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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Carroll, Chris; Cheng, Yeou-Yen; Meyer, Jon; Reysen, Bill; Twu, Bor-Long; Vanniasinkam, Joseph; Viray, Chris; Wai, John

Assignee: Unassigned

Title: Filter Based Multiplexer/Demultiplexer Component

Serial No.: 09/808,197      Filing Date: March 13, 2001

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Docket No.: M-9341 US

San Jose, California  
August 7, 2001

Attn: Official Draftsperson  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

Applicant(s) submit(s) sixteen (16) sheets of formal drawings, consisting of Figures 1A, 1B, 1C, 2A, 2B, 2C, 3A, 3B, 3C, 4A, 4B, 4C, 5A, 5B, 5C, 5D, 5E, 5F, 5G, 5H, 5I, 5J, 5K, 5L, 5M, 5N, 5O, 5P, 5Q, 5R, 6, 7A, 7B, 7C, and 7D, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

EL 701 022 094 US

Respectfully submitted,

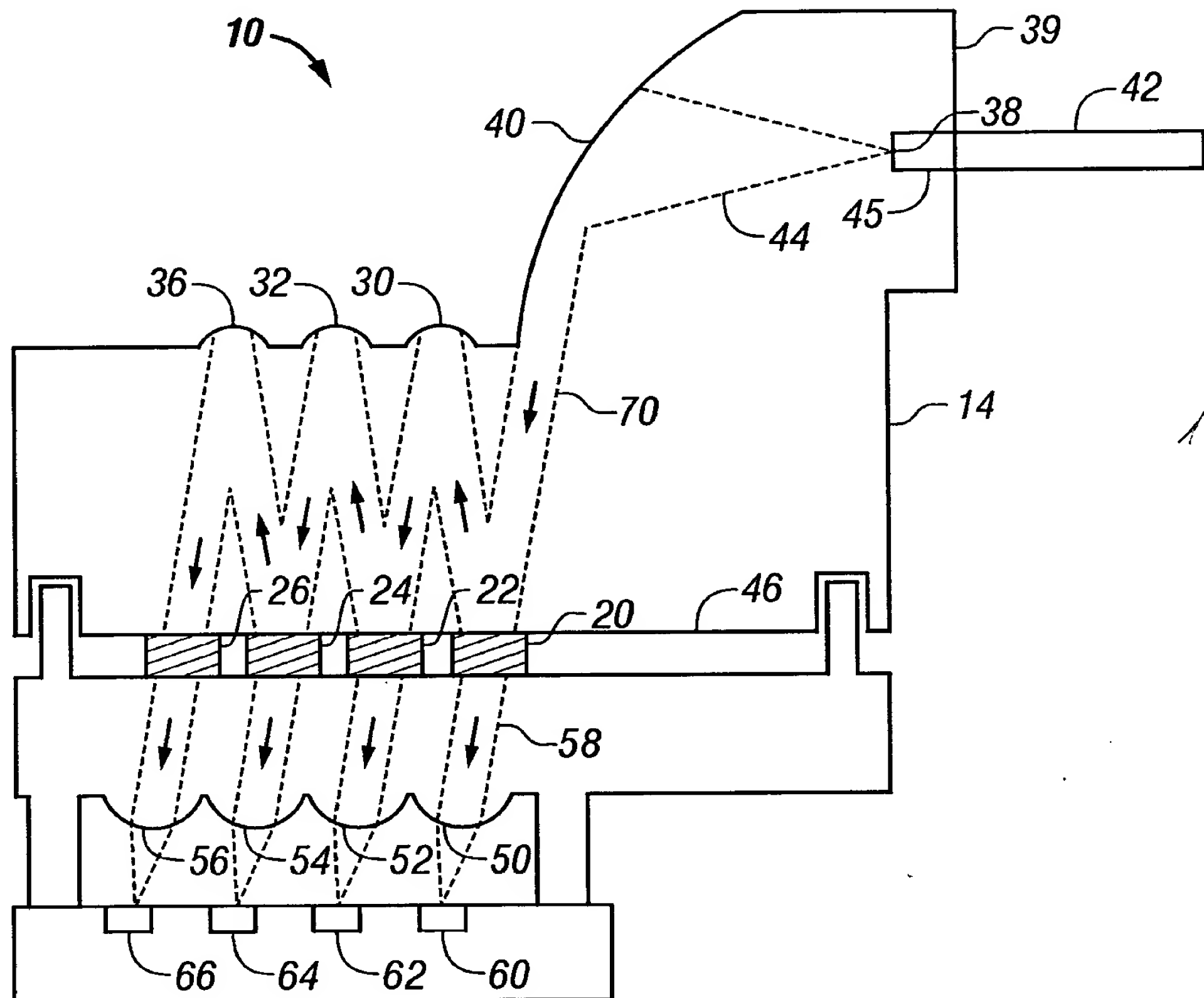
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Reg. No. 41,008

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**FIG. 1A**  
**(Prior Art)**

2/16

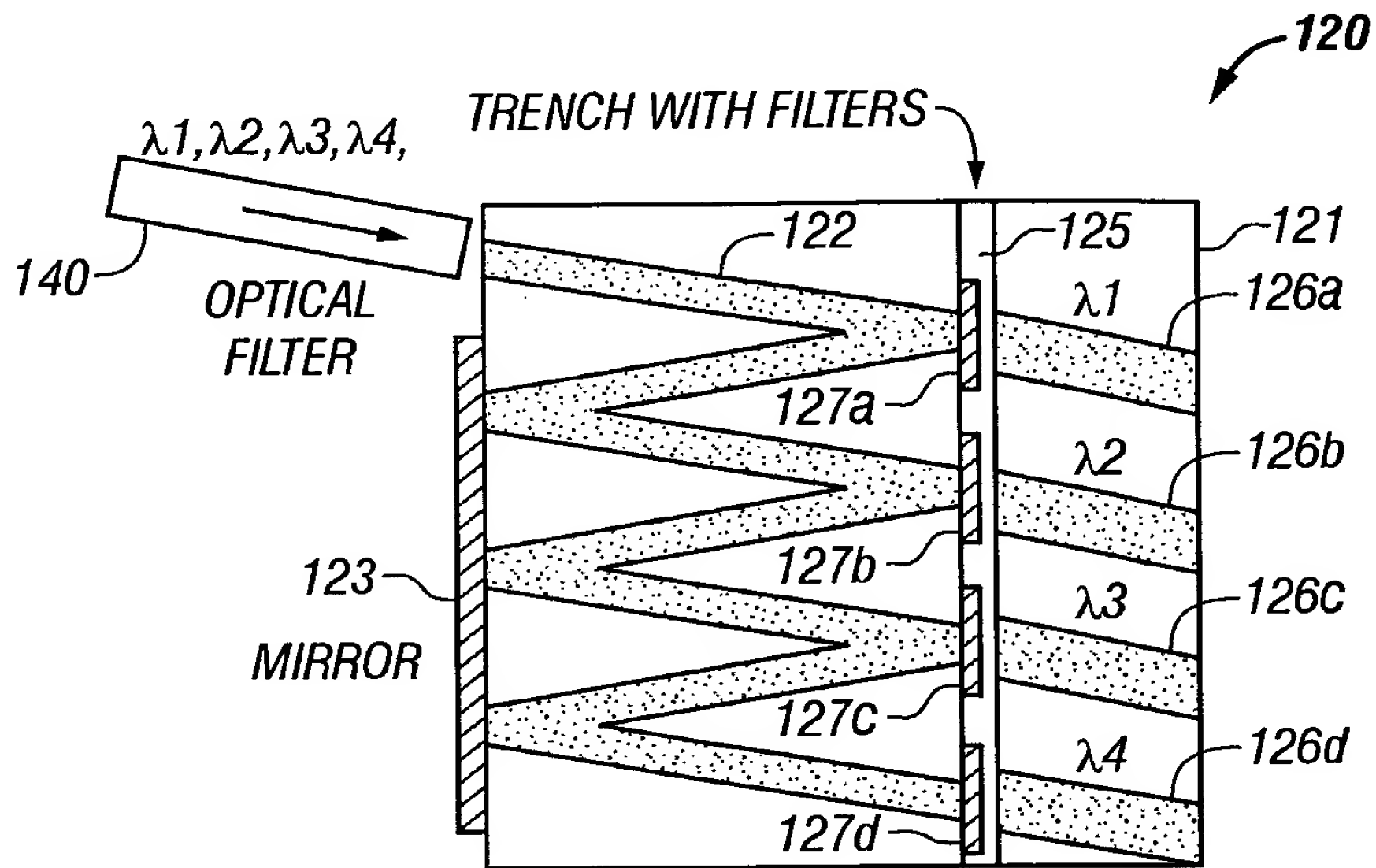


FIG. 1B

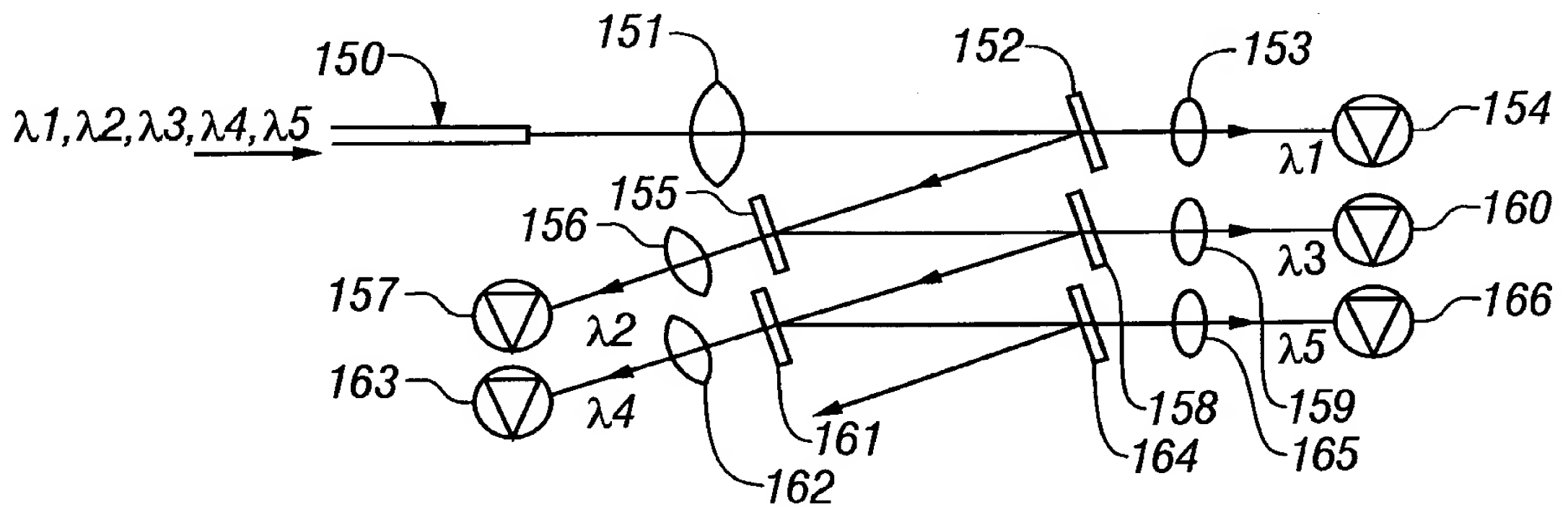
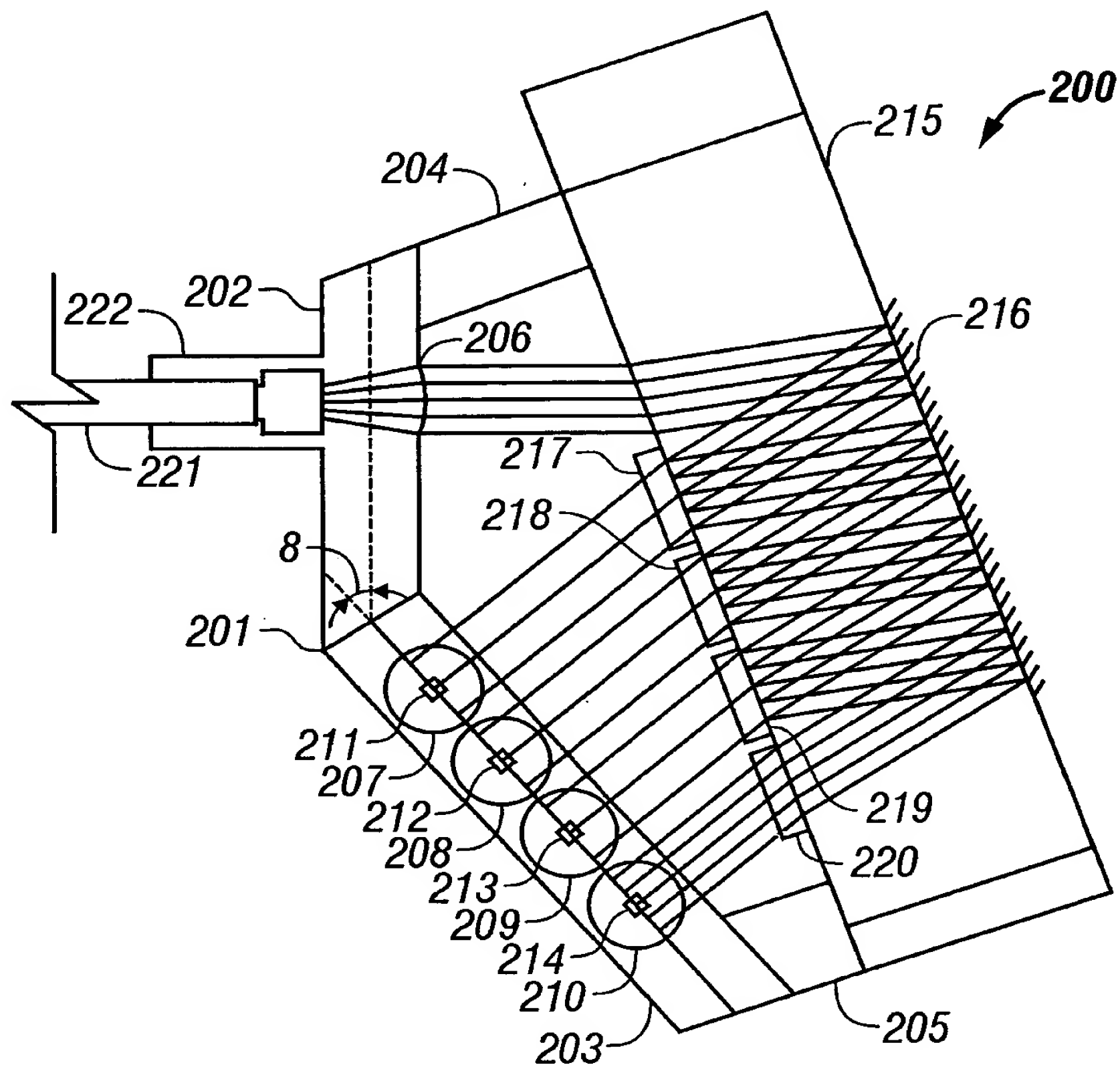
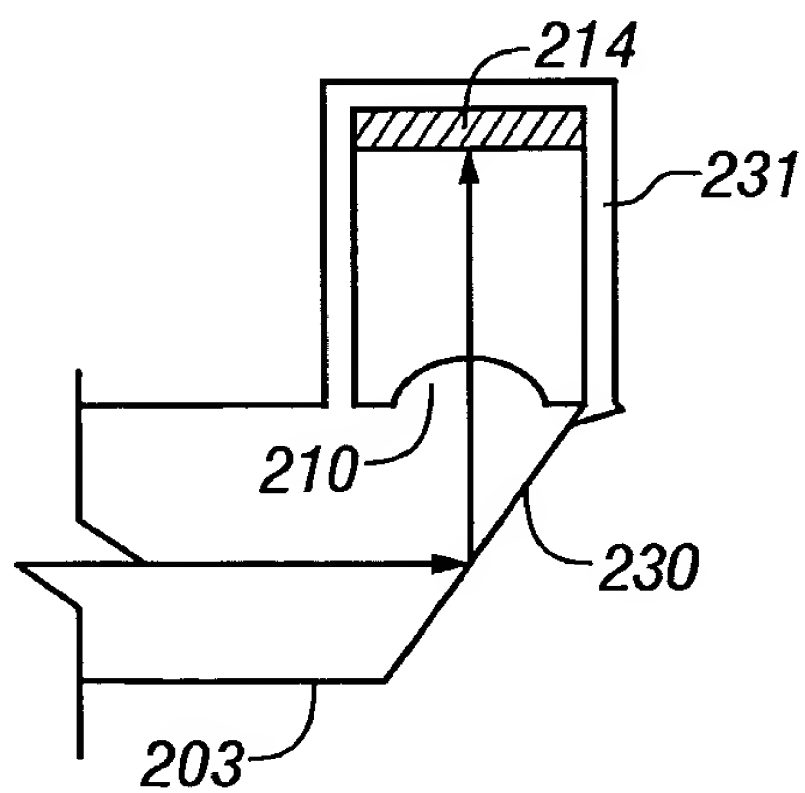
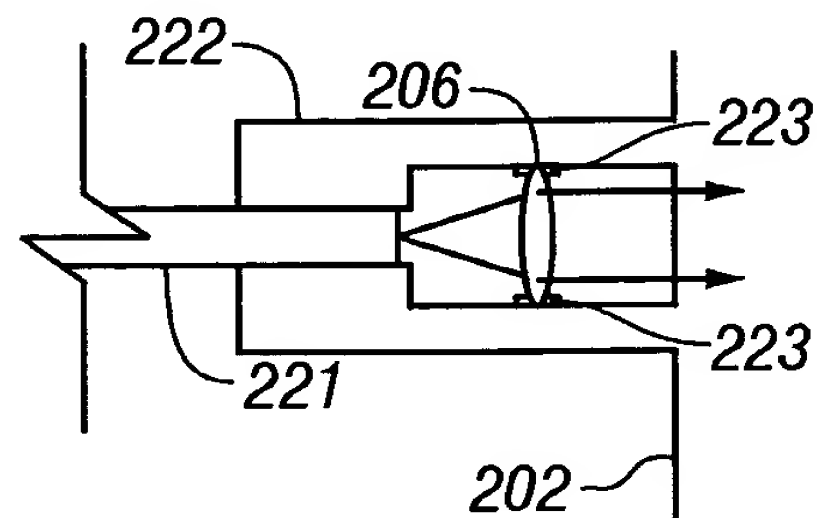


FIG. 1C

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**FIG. 2A**

**FIG. 2B**



**FIG. 2C**

4/16

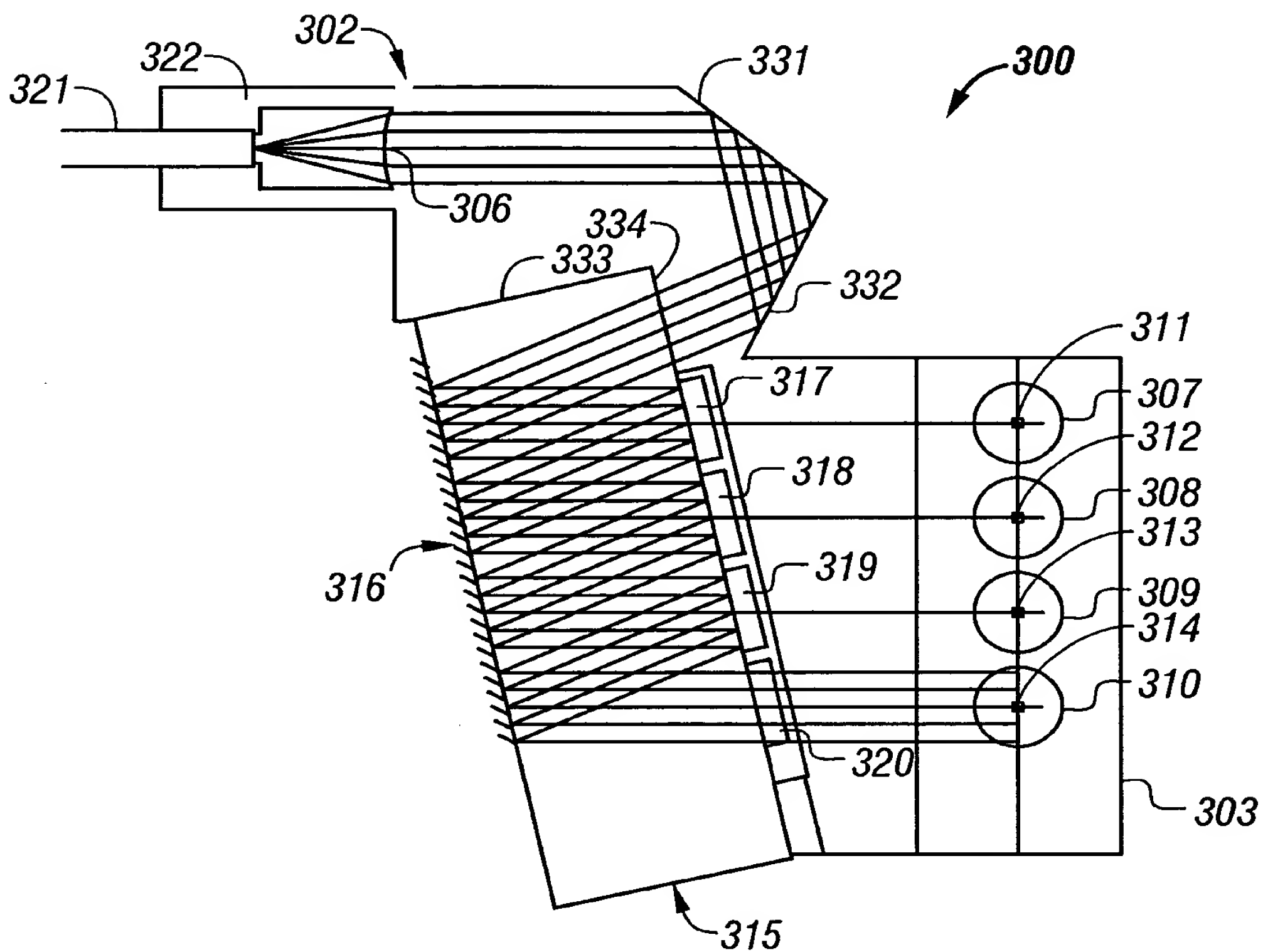


FIG. 3A

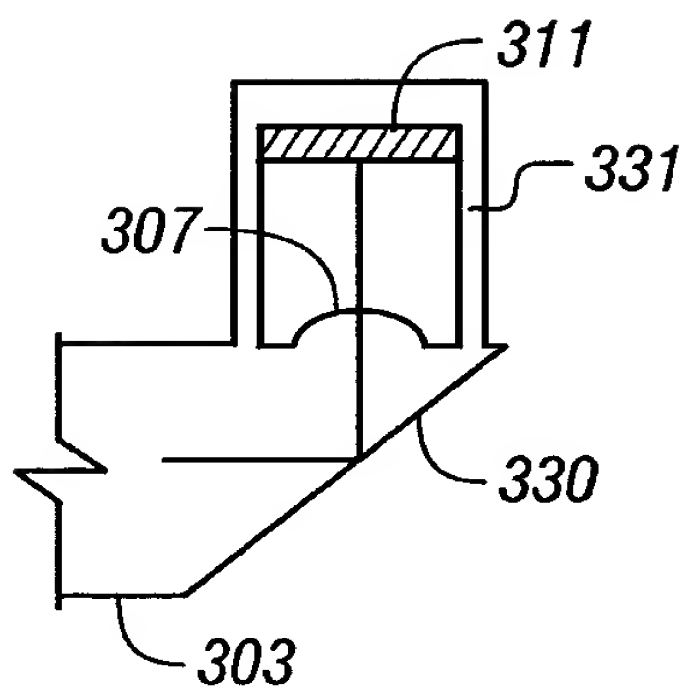


FIG. 3B

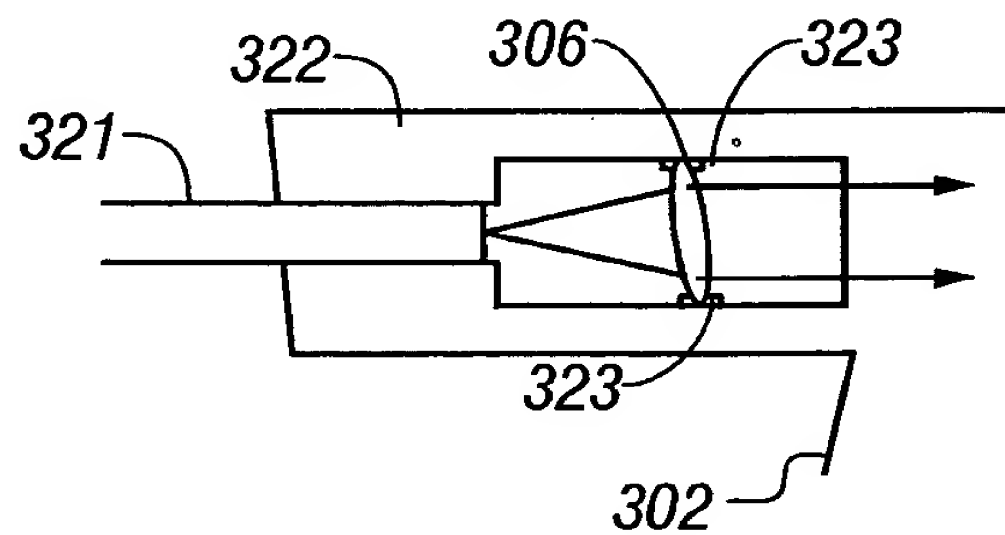


FIG. 3C

5/16

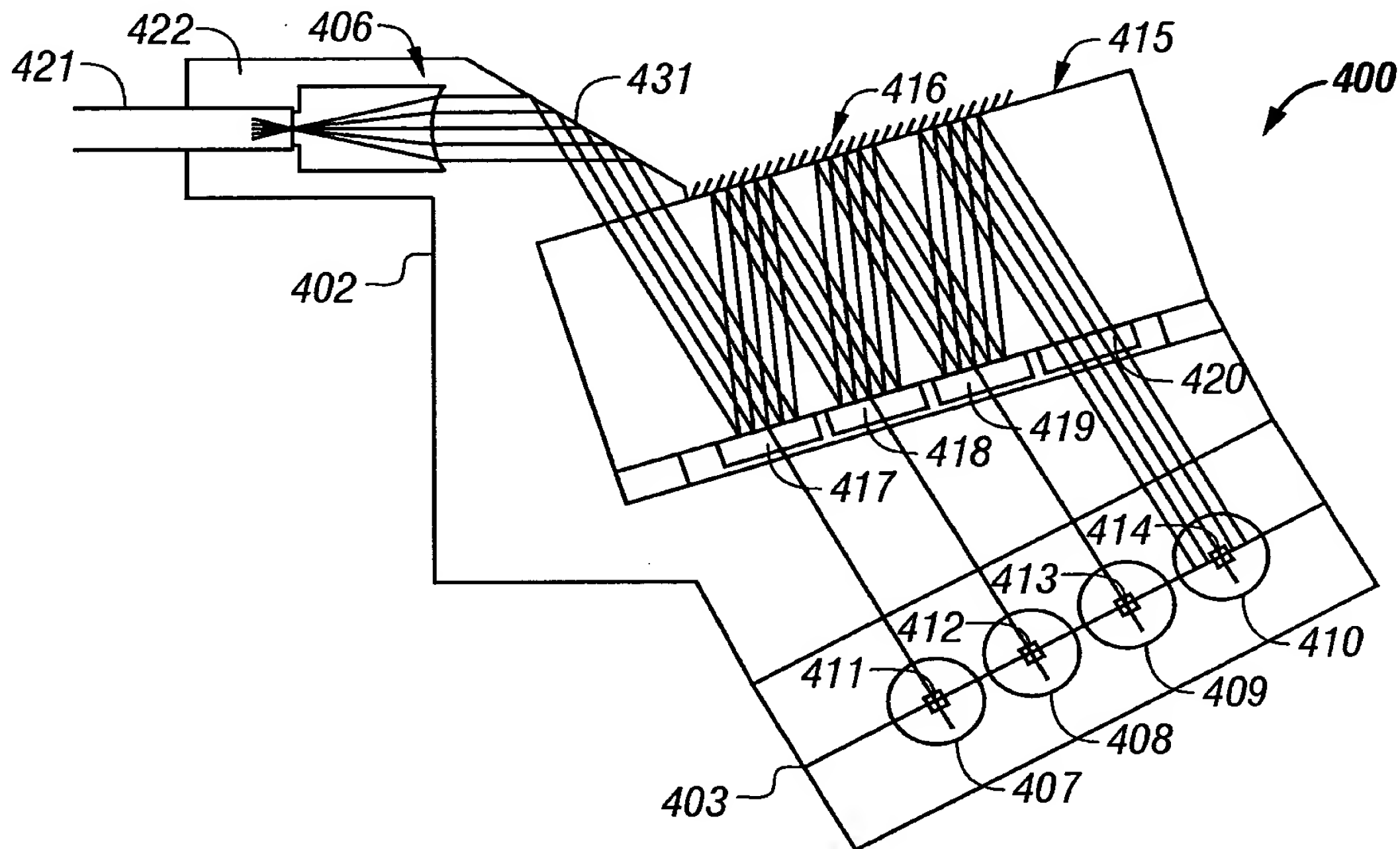


FIG. 4A

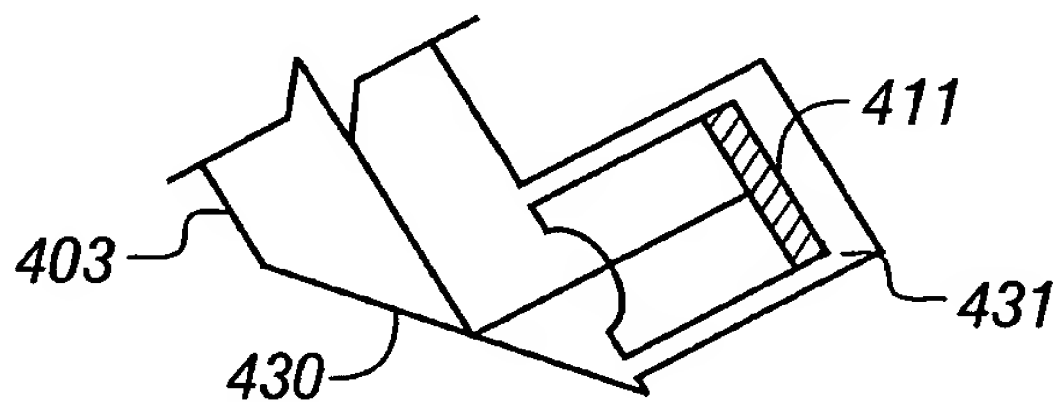


FIG. 4B

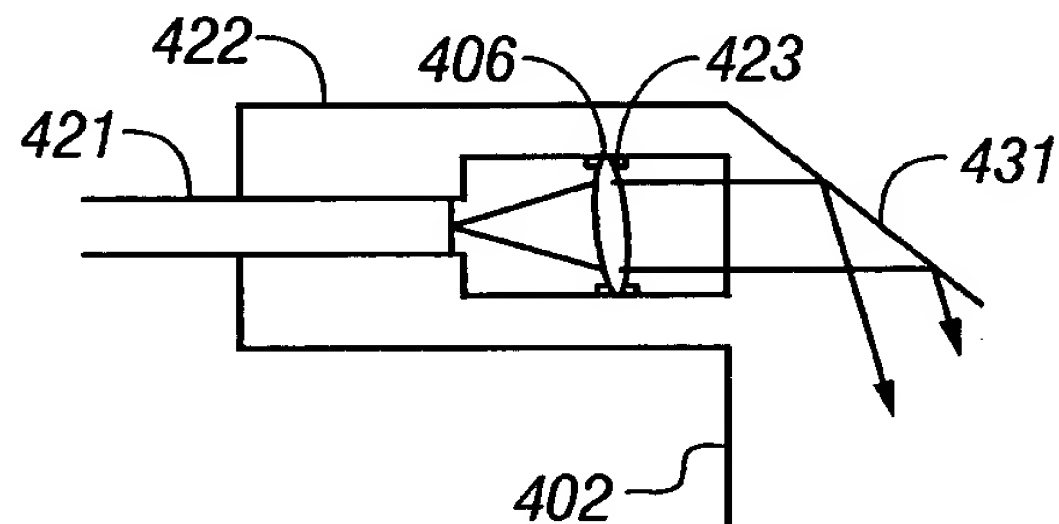


FIG. 4C

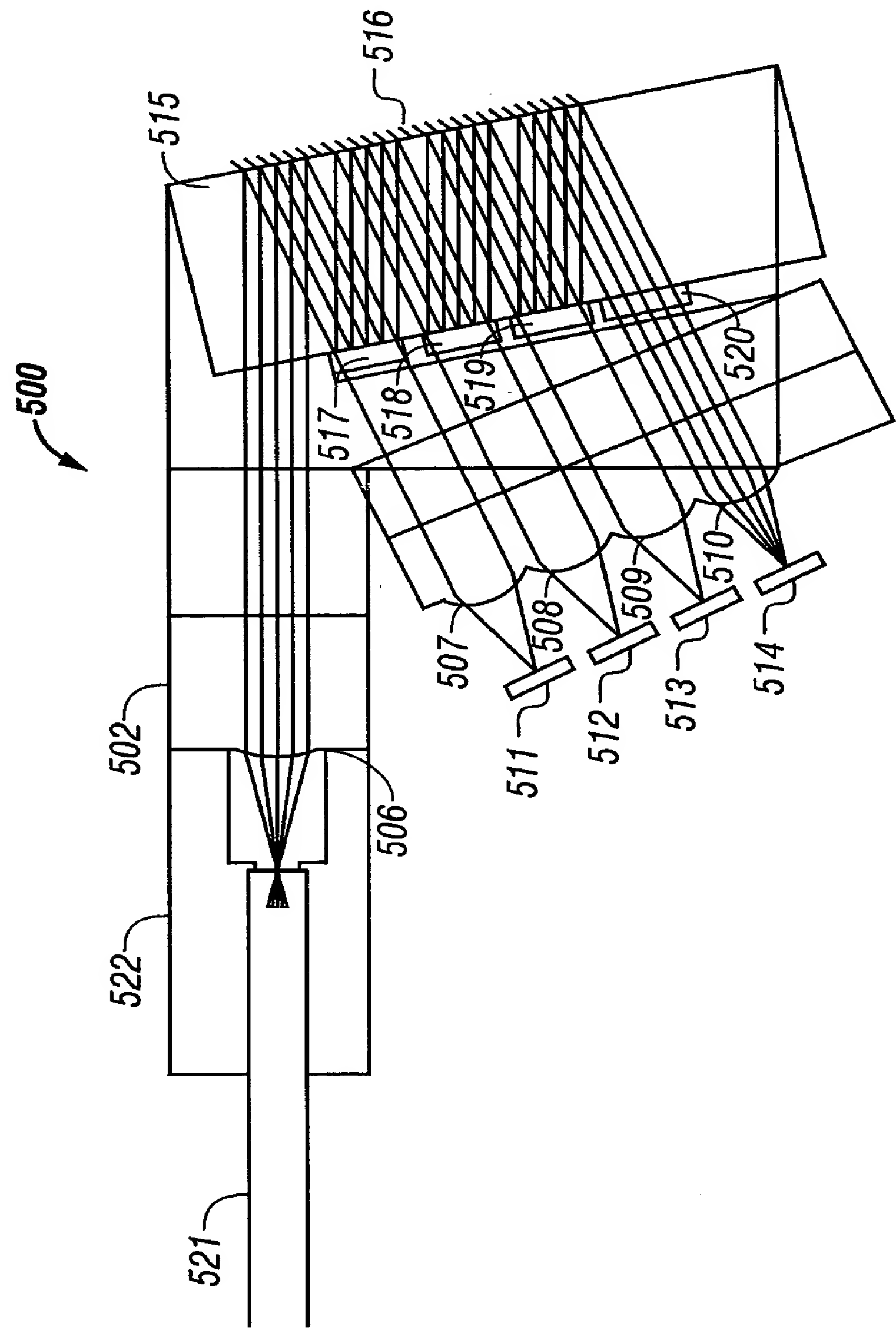


FIG. 5A





8/16

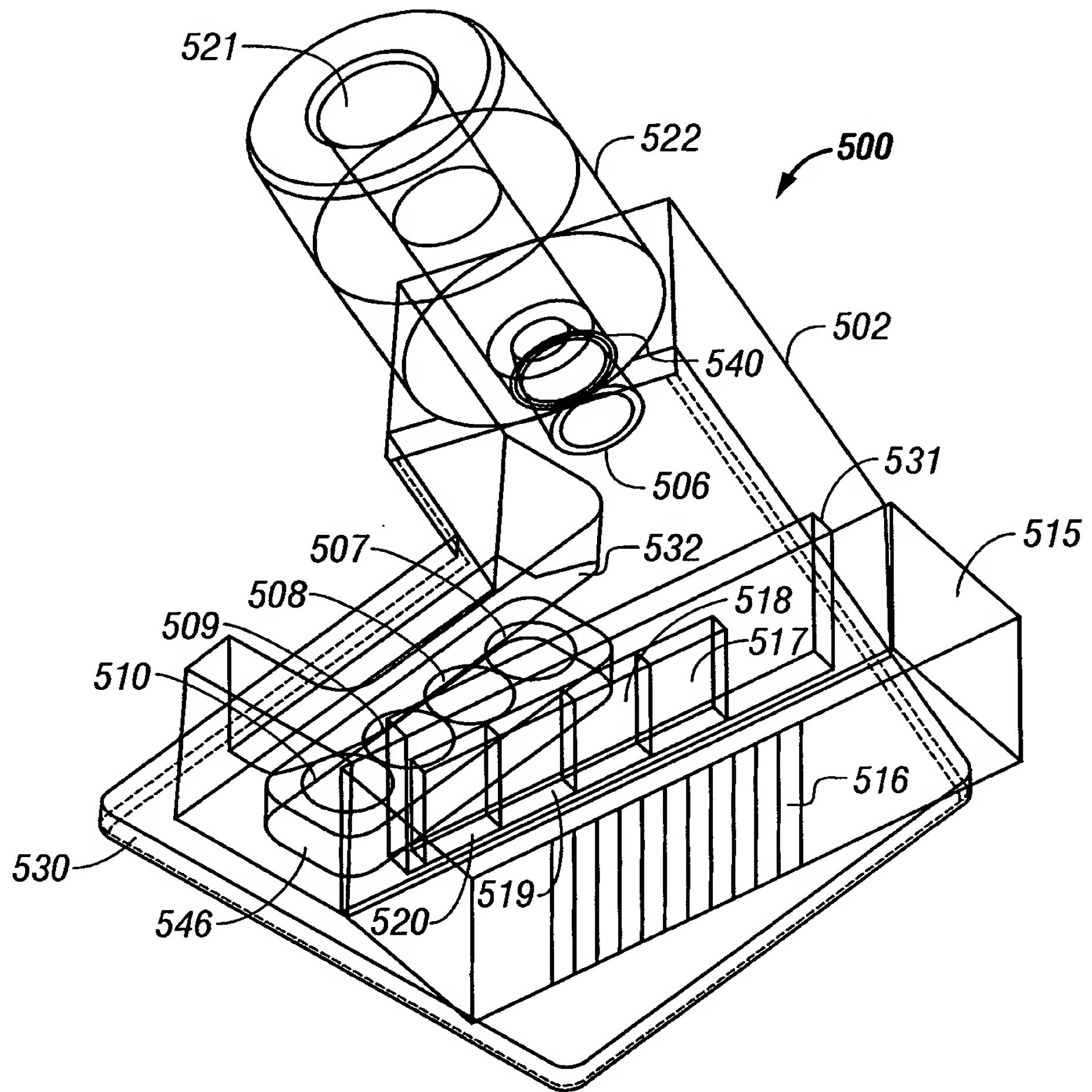


FIG. 5D

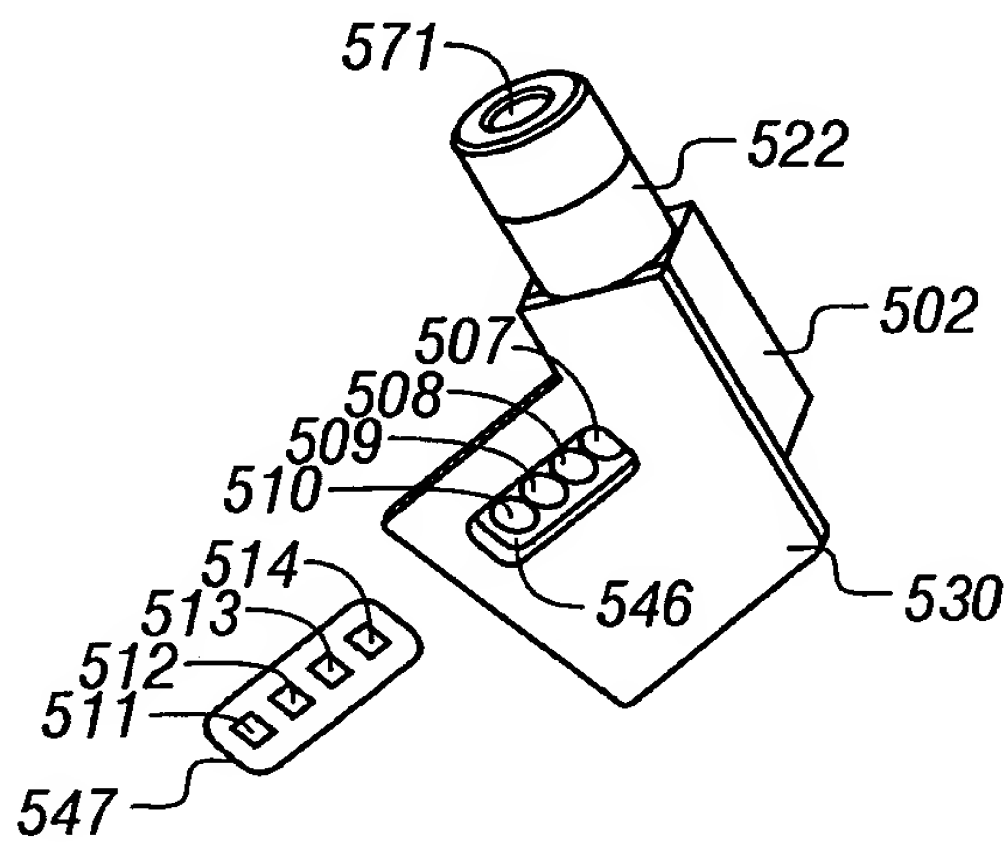


FIG. 5E

FIG. 5D

9/16

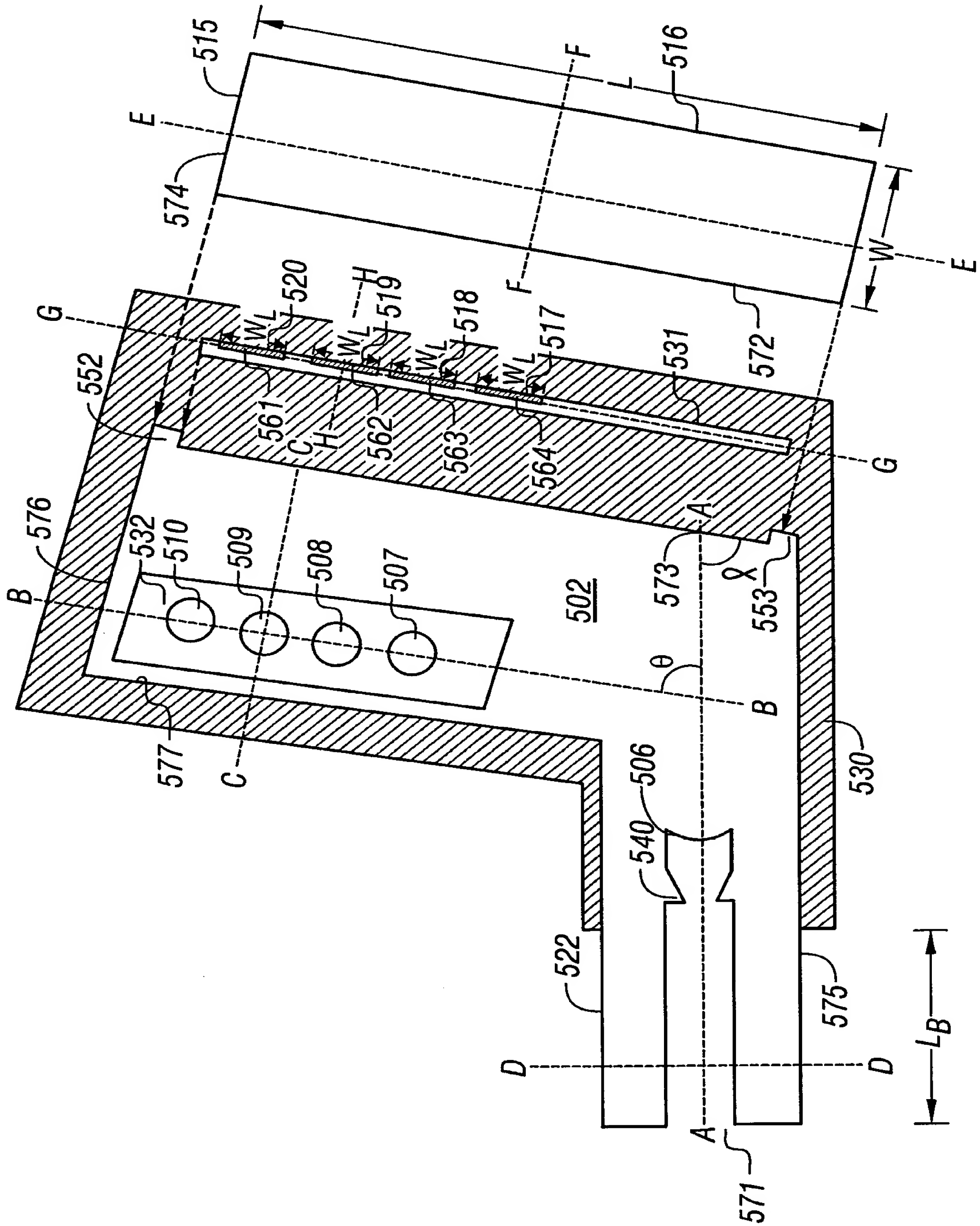


FIG. 5F

10/16

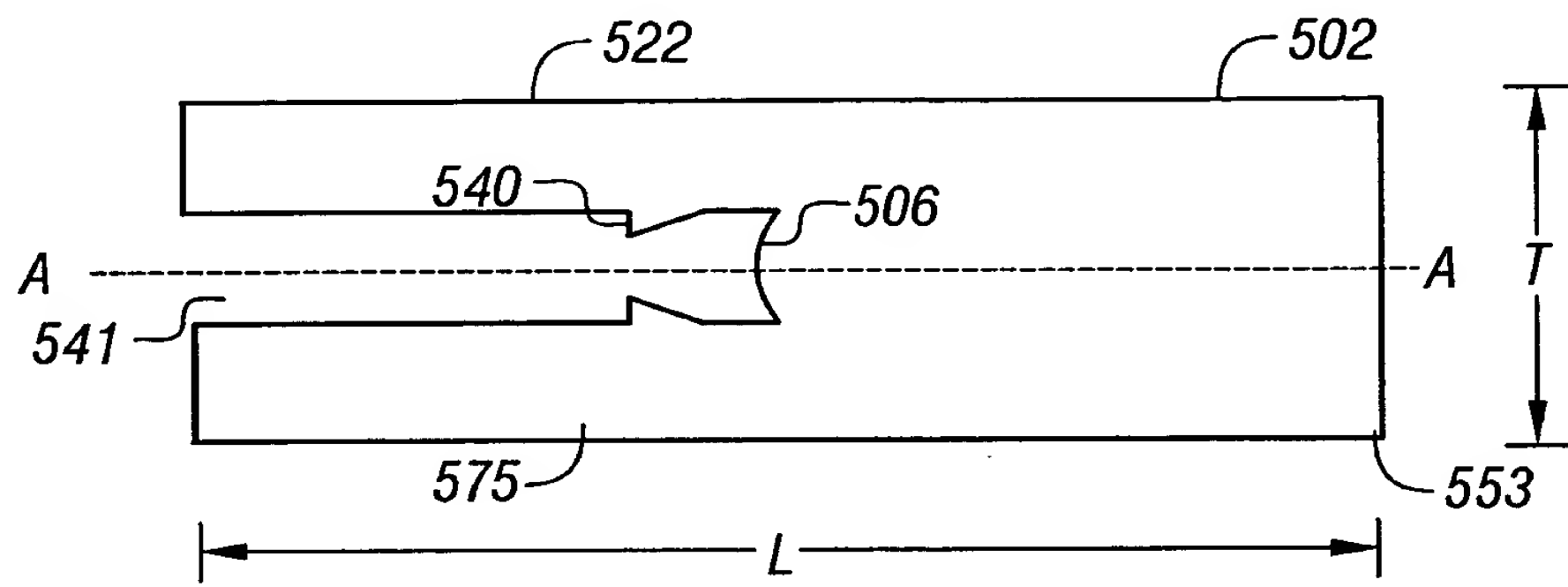


FIG. 5G

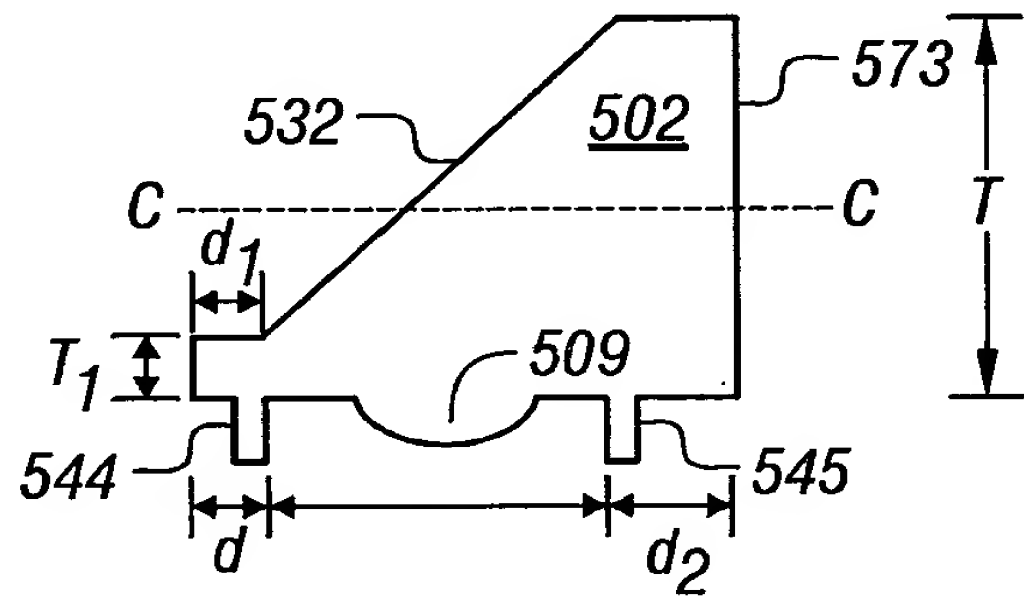


FIG. 5H

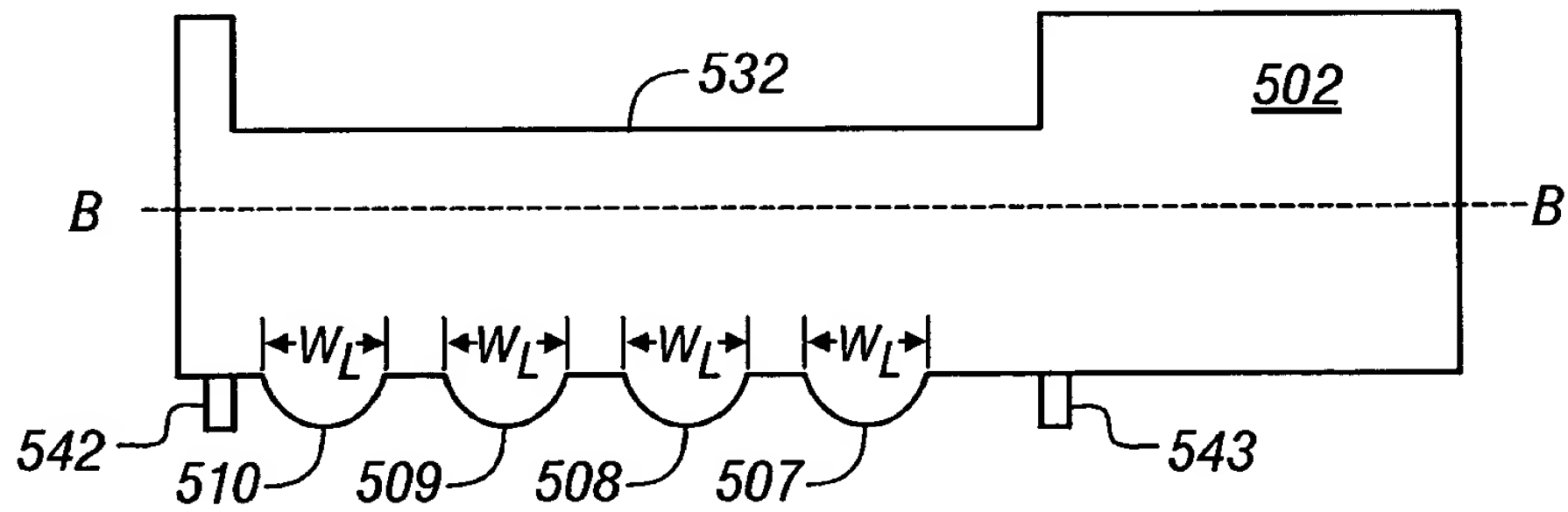


FIG. 5I

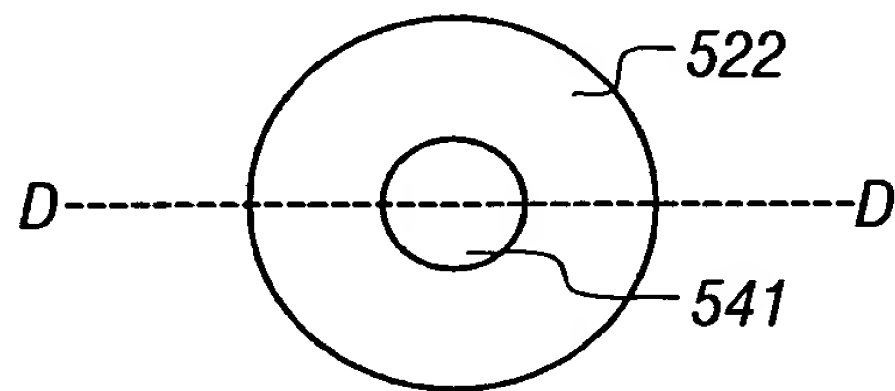


FIG. 5J

09808197-000701

11/16

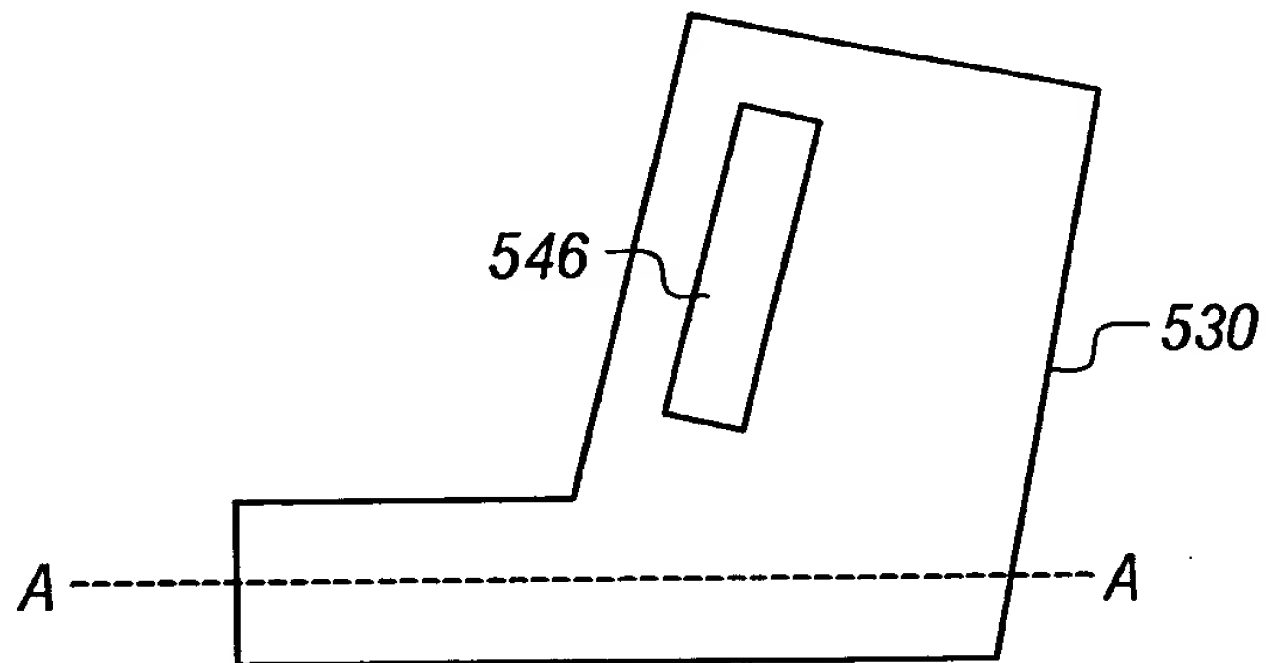


FIG. 5K

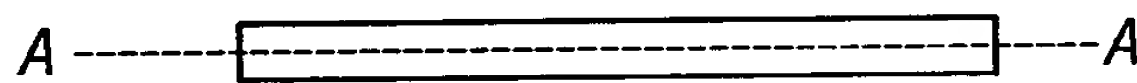


FIG. 5L

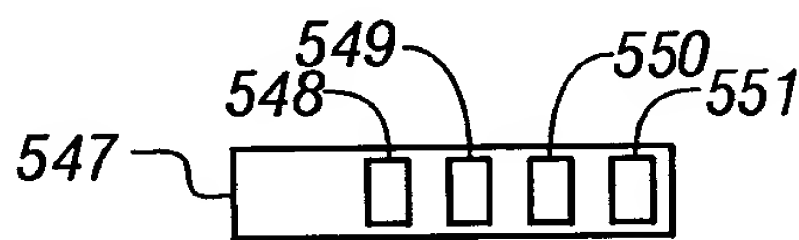


FIG. 5M

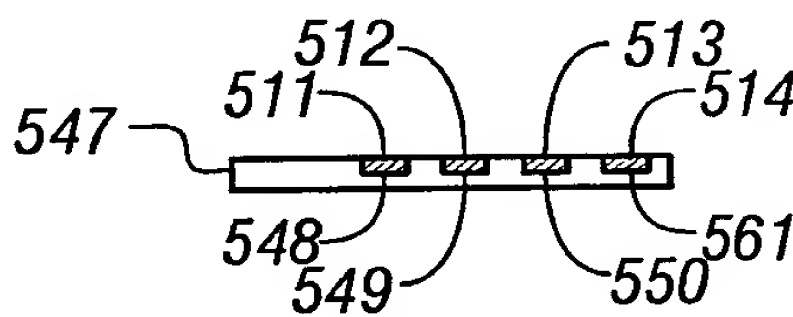


FIG. 5N

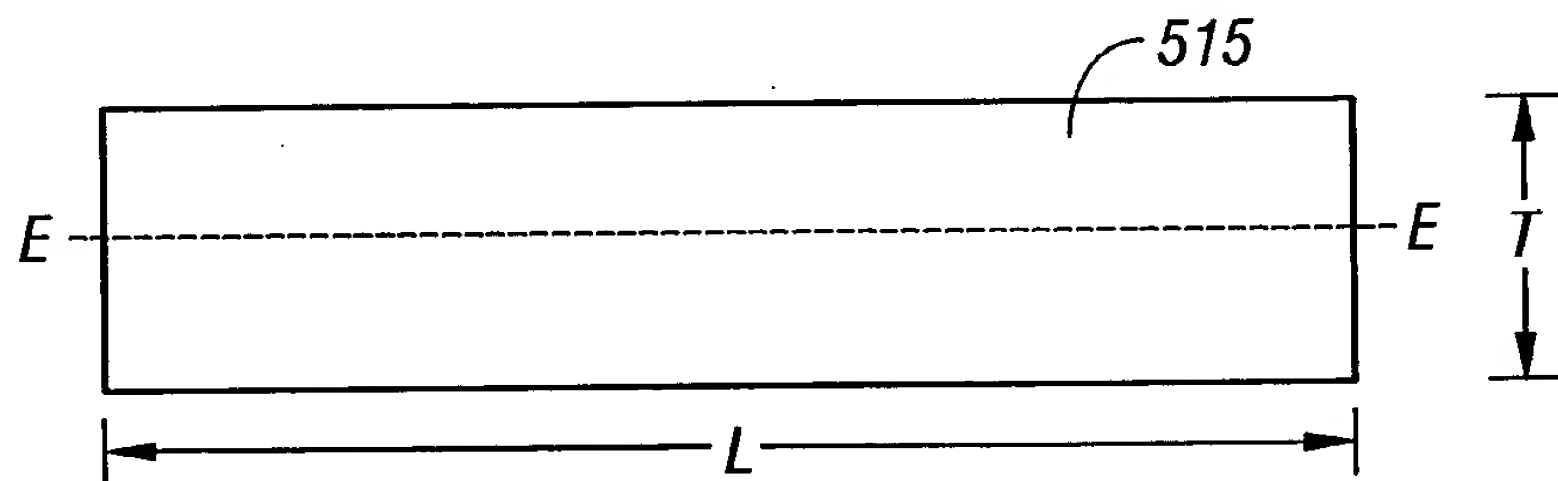


FIG. 5O

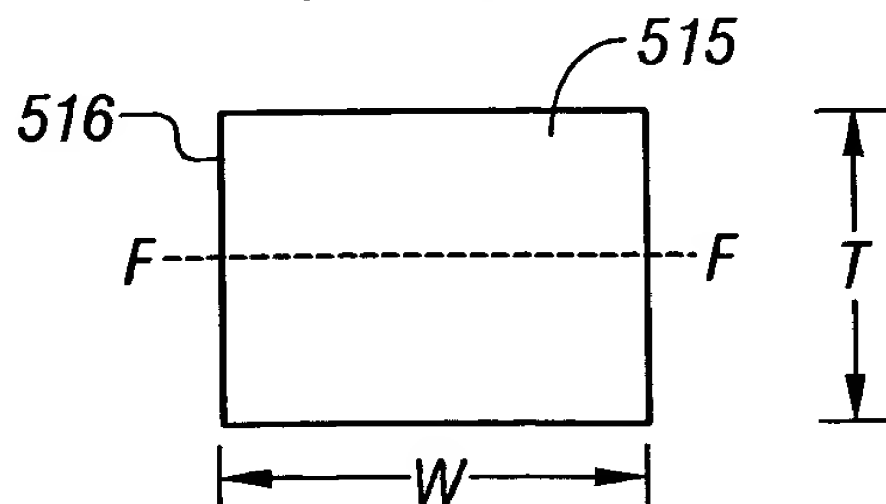


FIG. 5P

09808197-080701

12/16

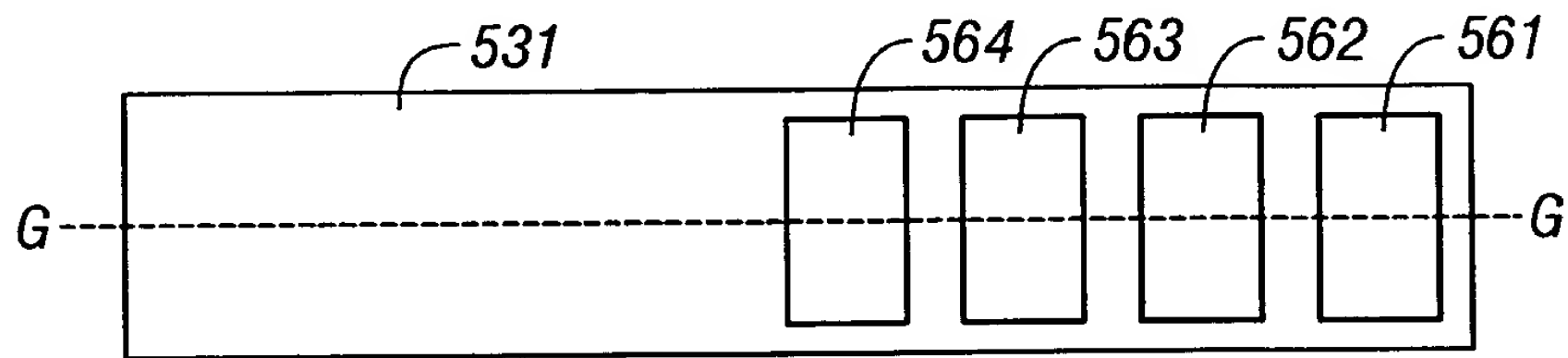


FIG. 5Q

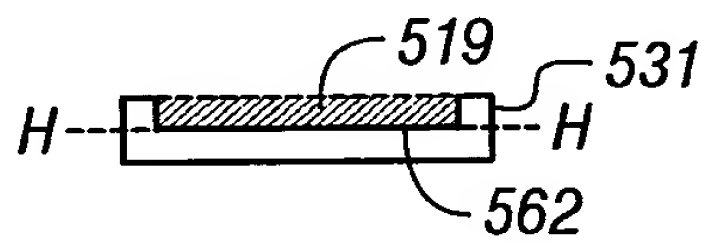


FIG. 5R

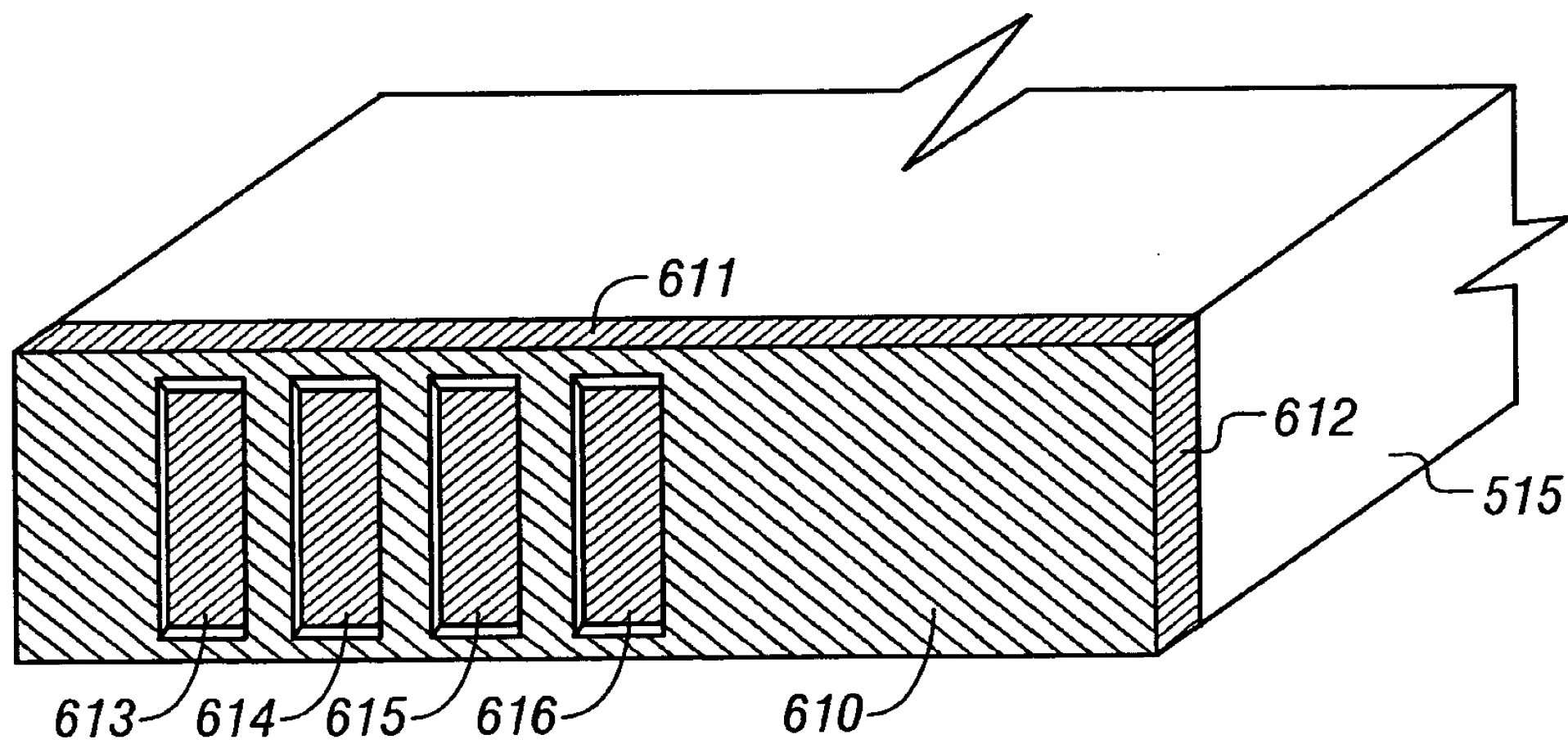


FIG. 6

13/16

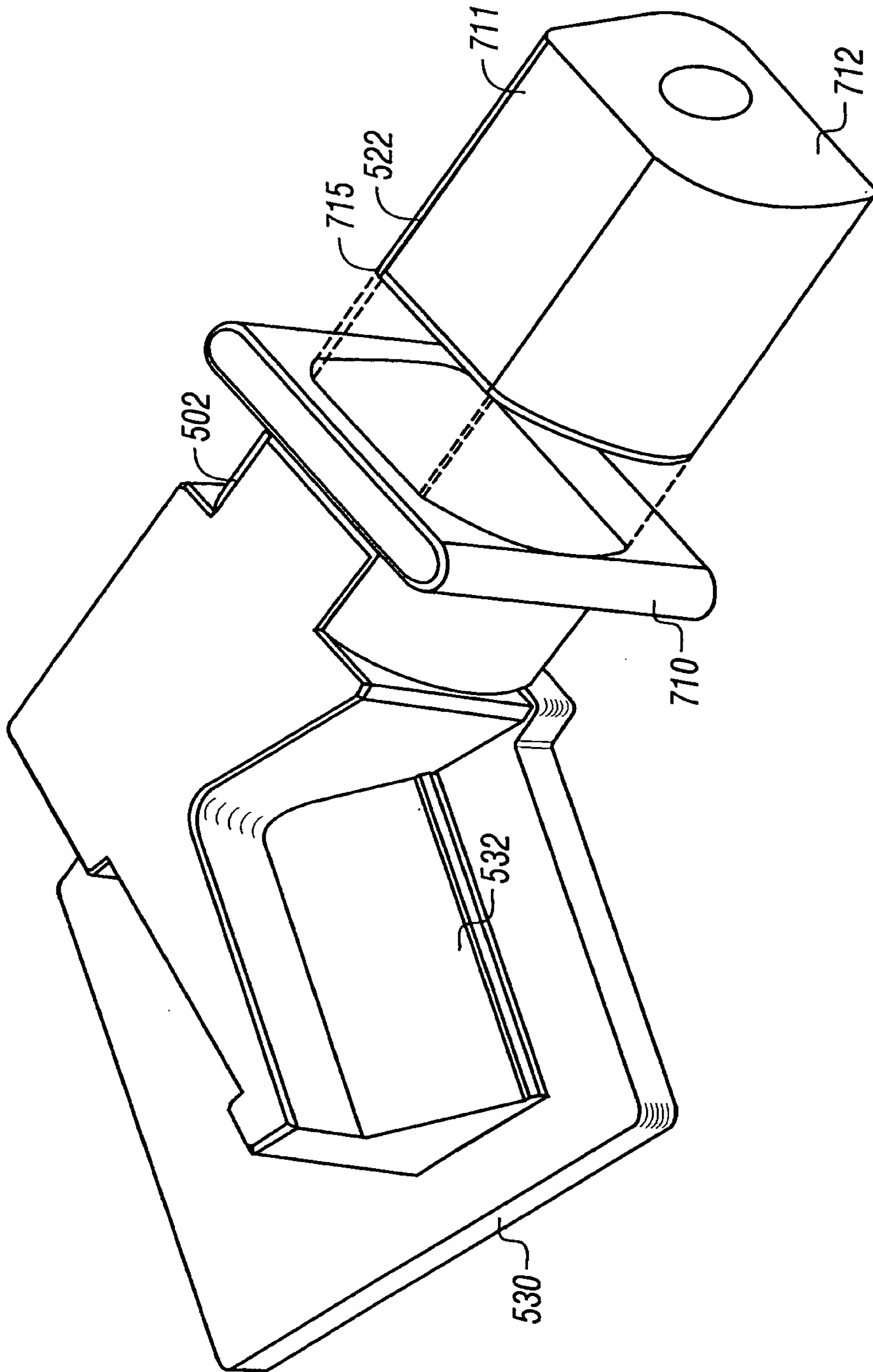


FIG. 7A

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14/16

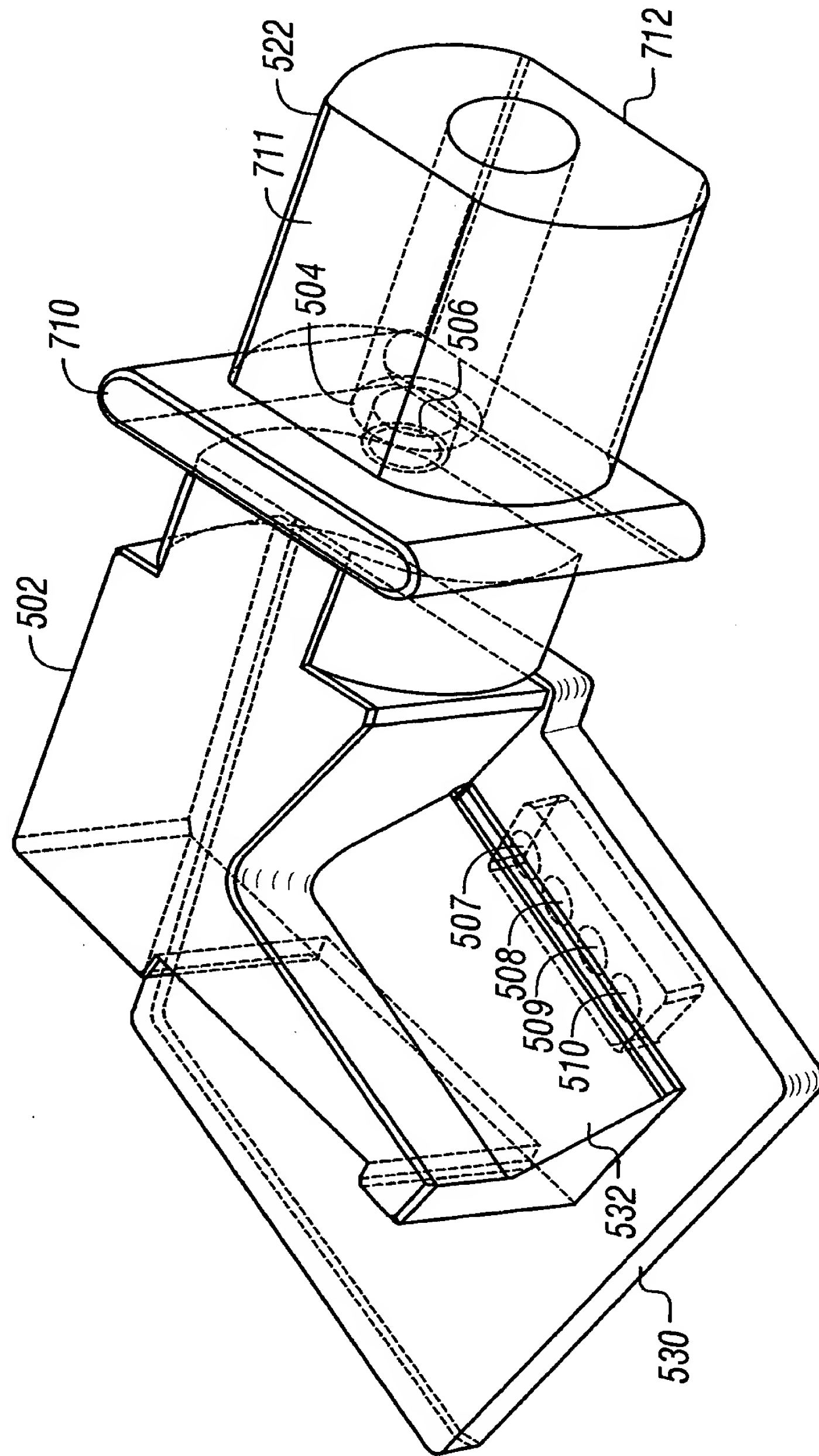


FIG. 7B

15/16

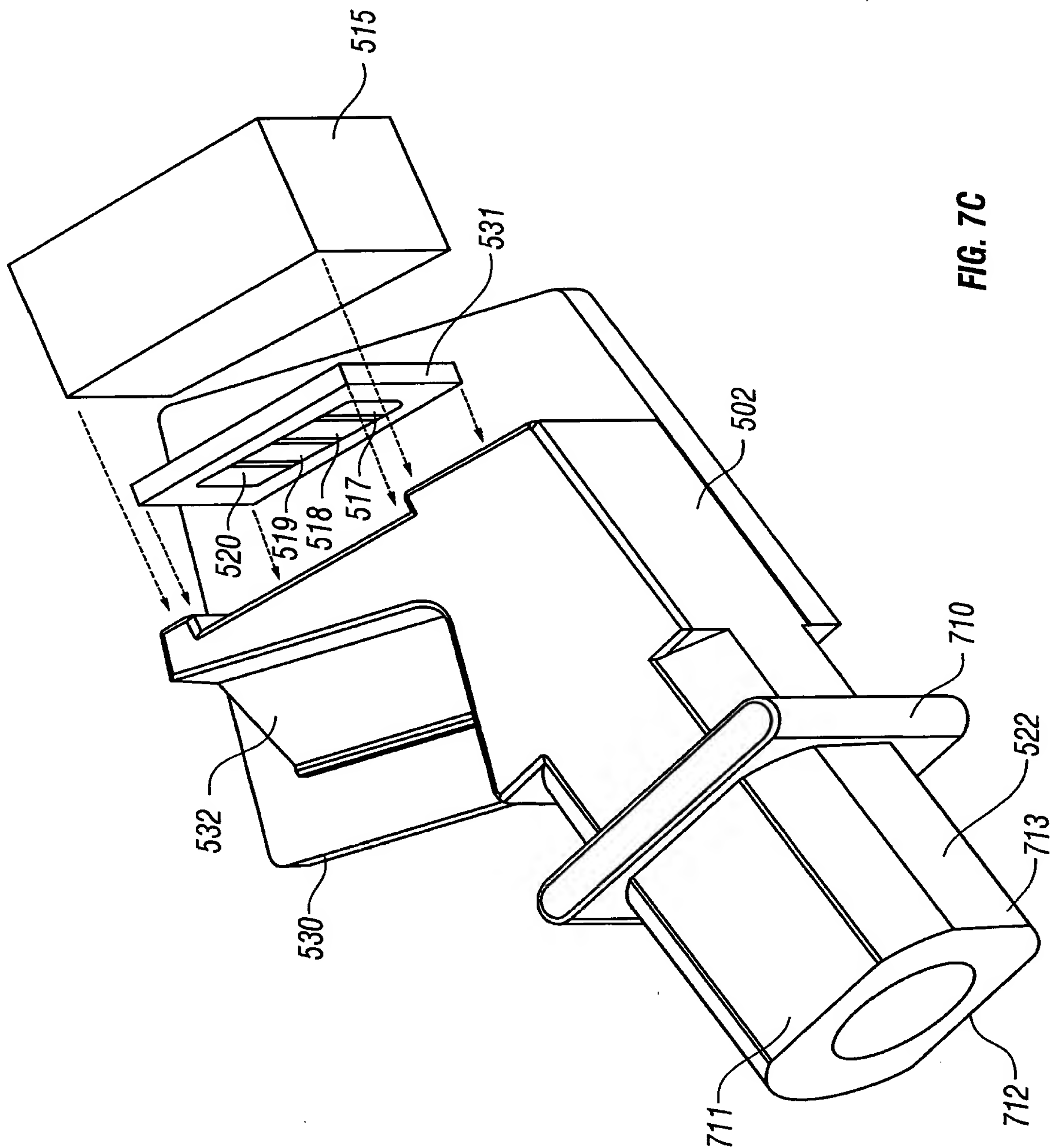


FIG. 7C

FIG. 7C



16/16

